IN THE ABSTRACT:

Please replace the Abstract of the Disclosure originally filed with the above-identified patent application with the following new Abstract of the Disclosure:

ABSTRACT OF THE DISCLOSURE

A multilayer PTC thermistor reliably decreases the resistance by decreasing the thickness of ceramic layers composed of a $BaTiO_3$ semiconductor ceramic and achieves a resistance close to the resistance calculated from the multilayer structure. The thermistor is adjusted to satisfy the conditions $5 \leq X \leq 18$ and $4 \leq X \cdot Y \leq 10$, wherein X is a thickness (μm) of each ceramic layer disposed between adjacent internal electrodes and Y is a donor content (%) in the barium titanate semiconductor ceramic constituting the ceramic layers, Y being expressed in terms of (number of donor atoms/number of Ti atoms) × 100.